Docket No. PD 1300.01 US   **PATENT**
USSN: 10/063,558   Art Unit: 2186

Do not enter (TH.)

LISTING OF CLAIMS:

Claim 1 (currently amended): An apparatus for efficiently running an execution image containing instructions for running a computer program, comprising:

a non-volatile memory configured to store a ~~compressed~~ version of said execution image;

a volatile memory configured to execute said execution image; ~~and~~

a decompression code in said non-volatile memory; and

a computing unit configured to transfer ~~and decompress~~ said ~~compressed~~ version of said execution image from said non-volatile memory to said volatile memory ~~where said execution image in non-compressed form can be executed efficiently~~ wherein said computing unit uses a first and an optional second header, said optional second header indicating whether said version of said execution image is a compressed version, and decompressing said compressed version using said decompression code, if so.

Claim 2 (original): The apparatus of claim 1, wherein said non-volatile memory is a FlashROM.

Claim 3 (original): The apparatus of claim 1, wherein said volatile memory is a DRAM.

Claim 4 (cancelled)

Claim 5 (cancelled)

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Claim 6 (currently amended): A method for efficiently running an execution image containing instructions for running a computer program [on a set-top box], comprising the steps of:

storing a ~~compressed~~ version of said execution image in a non-volatile memory;

storing a decompression code in said non-volatile memory;

using a first and an optional second header, said optional second header indicating whether said version of said execution image is a compressed version; and

decompressing said compressed version using said decompression code, if indicated by said second header; and

~~decompressing said compressed version of said execution image to obtain said execution image in non-compressed form;~~

executing said execution image in said volatile memory ~~whereby said execution image can thus be stored in a small-size in said non-volatile memory while also being executable at a faster execution speed in said volatile memory~~.

Claim 7 (original): The method of claim 6, wherein said non-volatile memory is a FlashROM.

Claim 8 (original): The method of claim 6, wherein said volatile memory is a DRAM.

Claim 9 (cancelled)

Claim 10 (cancelled)

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